

E | the plurality of processor devices including a transmitting processor device and a receiving processor device;  
a mailbox memory corresponding to the transmitting processor device containing information to be transferred to the receiving processor device; and  
the transmitting processor device being determined prior to the receiving processor device reading the information to be transferred.

2. (Twice Amended) An apparatus as in claim 1, wherein:  
[the plurality of processor devices includes a transmitting processor device and a receiving processor device;]  
a signal line provides communication between the processor devices; and  
a subroutine in the receiving processor device, corresponding to the signal line, reads [a] the mailbox memory corresponding to the transmitting processor device.

13. (Twice Amended) A method of communicating among a plurality of processor devices, including a transmitting processor device and a receiving processor device, utilizing a post office with a plurality of mailboxes, the method comprising the steps of:

E2 | writing information to be transferred from a transmitting processor device to a receiving processor device into a predetermined one of the mailboxes in the post office at any time determined by [a] the transmitting processor device;

signaling [a] the receiving processor device with the transmitting processor device;

determining in the receiving processor device, prior to the receiving processor reading the information, which of the processor devices signalled the receiving processor device; and

reading the information from the predetermined mailbox with the receiving processor device at a time determined by the receiving processor.

22. (Twice Amended) A method of bi-directional communication between at least two processor devices, utilizing a post office RAM with a plurality of mailboxes, the method comprising the steps of:

writing information to be transferred from a first processor device to a second processor device into a first mailbox in the post office RAM, and signaling [a] the second processor device with [a] the first processor device;

writing information to be transferred from the second processor device to a first processor device into a second mailbox in the post office RAM, and signaling the first processor device with the second processor device;

determining in the second processor device, prior to the second processor device reading the first mailbox, which of the processor devices signalled the second processor device;

determining in the first processor device, prior to the first processor device reading the second mailbox, which of the processor devices signalled the first processor device;

reading the information in the first mailbox with the second processor device; and

reading the information in the second mailbox with the first processor device.